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Electronic Version v1.1

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Title of Invention	BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE							
Application Number : Date : First Named Applicant: Confirmation Number: Attorney Docket Number:	10/069229 2002-02-22 Wolrich Gilbert 1429 10559-305US1							
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<table border="1"><thead><tr><th data-bbox="133 993 773 1031">Submitted By:</th><th data-bbox="773 993 1110 1031">Elec. Sign.</th><th data-bbox="1110 993 1412 1031">Sign. Capacity</th></tr></thead><tbody><tr><td data-bbox="133 1031 773 1108">Scott C. Harris Registered Number: 32,030</td><td data-bbox="773 1031 1110 1108">/SCH/</td><td data-bbox="1110 1031 1412 1108">Attorney</td></tr></tbody></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Scott C. Harris Registered Number: 32,030	/SCH/	Attorney
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Documents being submitted:	Files
us-ids	10559-305US1_3-usidst.xml us-ids.dtd us-ids.xsl
Comments	
IDS # 3 of 4 for U.S. Application Serial No. 10/069,229	